

ABSTRACT OF THE DISCLOSURE

An information processing system includes a first processor having a first local memory, a second processor having a second local memory, and a third processor having a third local memory. The system further includes a unit which maps one of the second and third local memories in part of an effective address space of a first thread to be executed by the first processor, the one of the second and third local memories being the local memory of a corresponding one of the second and third processors, which executes a second thread interacting with the first thread, and a unit which changes the one of the second and third local memories which is to be mapped in part of the effective address space to the other when one of the second and third processor that executes the second thread is changed to the other.